

WEST Search History

DATE: Tuesday, July 10, 2007

Hide?	Set Name	Query	Hit Count
	DB=PGPB,USPT,USOC; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L62	((source driver) same (voltage setting)) and comparator	3
<input type="checkbox"/>	L61	((source driver) same (voltage setting))	21
<input type="checkbox"/>	L60	L55 and ((source driver) same (voltage setting))	0
<input type="checkbox"/>	L59	L54 and ((source driver) same (voltage setting))	6
<input type="checkbox"/>	L58	L55 and (("D/A" converter) same (N current))	0
<input type="checkbox"/>	L57	L55 and (("D/A" converter) and (current driver))	2
<input type="checkbox"/>	L56	L55 and (("D/A" converter) same (current driver))	0
<input type="checkbox"/>	L55	L54 and (("EL") or (electro-luminescence) or (organic light emitting) or (light emitting))	737
<input type="checkbox"/>	L54	(345/98,99,100)!{CCLS}	3268
<input type="checkbox"/>	L53	L52 and comparator	6
<input type="checkbox"/>	L52	L49 and (source driver)	55
<input type="checkbox"/>	L51	L49 and (dummy circuit)	11
<input type="checkbox"/>	L50	L49 same (dummy circuit)	5
<input type="checkbox"/>	L49	(dummy pixel)	643
<input type="checkbox"/>	L48	L43 and (dummy pixel)	9
	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L47	(20030011408 6225844 20030048125 20050088396 20050068076 5594373 20030132930 6897619 6037807 5856749)!{PN}	10
	DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L46	("6958742")!{PN}	1
<input type="checkbox"/>	L45	("6958742")!{PN}.p67-p150.	1
	DB=PGPB,USPT,USOC; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L44	L43 and ("D/A" converter)	18
<input type="checkbox"/>	L43	L34 and (current driver)	93
	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L42	L41 and ("D/A" converter)	0
<input type="checkbox"/>	L41	L15 and (current driver)	24
	DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L40	L35 and (current driver)	6
<input type="checkbox"/>	L39	((dummy circuit) same (voltage)) and display	22

<input type="checkbox"/>	L4	matsushita-electric-industrial\$.as.	32394
DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ			
<input type="checkbox"/>	L3	L1 and L2	2
<input type="checkbox"/>	L2	date-yoshito\$.in.	31
<input type="checkbox"/>	L1	oomori-tetsuro\$.in.	2

END OF SEARCH HISTORY

<input type="checkbox"/>	L38	(dummy circuit) and display	173
<input type="checkbox"/>	L37	(dummy circuit) and L35	1
<input type="checkbox"/>	L36	(dummy circuit) and L34	2
<input type="checkbox"/>	L35	((("EL") or (electro-luminescence) or (organic light emitting) or (light emitting)) and (register\$ same ("N" bits) and ("M" bits)))	119
<input type="checkbox"/>	L34	((("EL") or (electro-luminescence) or (organic light emitting) or (light emitting)) same register\$	6028
	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L33	("D/A" converter) and L27	87
<input type="checkbox"/>	L32	("D/A" converter) and L26	10
<input type="checkbox"/>	L31	("D/A" converter) and L25	87
<input type="checkbox"/>	L30	("D/A" converter) and L24	15
<input type="checkbox"/>	L29	("D/A" converter) and L23	12
<input type="checkbox"/>	L28	("D/A" converter) and L22	31
<input type="checkbox"/>	L27	((EL) or (electro-luminescence)) and L16	442
<input type="checkbox"/>	L26	((EL) or (electro-luminescence)) and L15	102
<input type="checkbox"/>	L25	((EL) or (electro-luminescence)) and L14	521
<input type="checkbox"/>	L24	((OLE) or (organic light emitting)) and L16	115
<input type="checkbox"/>	L23	((OLE) or (organic light emitting)) and L15	105
<input type="checkbox"/>	L22	((OLE) or (organic light emitting)) and L14	308
<input type="checkbox"/>	L21	(register\$ same latching) and L13	2
<input type="checkbox"/>	L20	(register\$ same latching) and L12	84
<input type="checkbox"/>	L19	(register\$ same latching) and L11	15
<input type="checkbox"/>	L18	(register\$ same latching) and L10	47
<input type="checkbox"/>	L17	register\$ and L13	36
<input type="checkbox"/>	L16	register\$ and L12	1559
<input type="checkbox"/>	L15	register\$ and L11	264
<input type="checkbox"/>	L14	register\$ and L10	963
<input type="checkbox"/>	L13	(345/692)!{CCLS}	89
<input type="checkbox"/>	L12	(345/204 345/690 345/692)!{CCLS}	4159
<input type="checkbox"/>	L11	(345/82)!{CCLS}	894
<input type="checkbox"/>	L10	(345/55 345/76 345/82)!{CCLS}	3131
	DB=PGPB,USPT,EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L9	L8 not L6	0
<input type="checkbox"/>	L8	L7 and display	1
<input type="checkbox"/>	L7	L5 and ((latching same data).clm.)	15
<input type="checkbox"/>	L6	L5 and ((latching display data).clm.)	1
<input type="checkbox"/>	L5	L4 and (register.clm.)	890

WEST Search History

Hide Items Resibore Clear Cancel

DATE: Tuesday, July 10, 2007

Hide?	Set Name	Query	Hit Count
<input type="checkbox"/>	L54	L53 and comparator	6
<input type="checkbox"/>	L53	L50 and (source driver)	55
<input type="checkbox"/>	L52	L50 and (dummy circuit)	11
<input type="checkbox"/>	L51	L50 same (dummy circuit)	5
<input type="checkbox"/>	L50	(dummy pixel)	643
<input type="checkbox"/>	L49	L44 and (dummy pixel)	9
<input type="checkbox"/>	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L48	(20030011408 6225844 20030048125 20050088396 20050068076 5594373 20030132930 6897619 6037807 5836749)[PN]	10
<input type="checkbox"/>	DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L47	("6958742")[PN]	1
<input type="checkbox"/>	L46	("6958742")[PN]. p67-p150.	1
<input type="checkbox"/>	DB=PGPB,USPT,USOC; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L45	L44 and ("D/A" converter)	18
<input type="checkbox"/>	L44	L34 and (current driver)	93
<input type="checkbox"/>	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L43	L42 and @pd > 20070502	0
<input type="checkbox"/>	L42	L41 and ("D/A" converter)	0
<input type="checkbox"/>	L41	L15 and (current driver)	24
<input type="checkbox"/>	DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L40	L35 and (current driver)	6
<input type="checkbox"/>	L39	((dummy circuit) same (voltage)) and display	22
<input type="checkbox"/>	L38	(dummy circuit) and display	173
<input type="checkbox"/>	L37	(dummy circuit) and L35	1
<input type="checkbox"/>	L36	(dummy circuit) and L34	2
<input type="checkbox"/>	L35	("EL") or (electro-luminescence) or (organic light emitting) or (light emitting) and (register\$ same ("N" bits) and ("M" bits))	119
<input type="checkbox"/>	L34	("EL") or (electro-luminescence) or (organic light emitting) or (light emitting) same register\$	6028
<input type="checkbox"/>	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L33	("D/A" converter) and L27	87

<input type="checkbox"/>	L32	("D/A" converter) and L26	10
<input type="checkbox"/>	L31	("D/A" converter) and L25	87
<input type="checkbox"/>	L30	("D/A" converter) and L24	15
<input type="checkbox"/>	L29	("D/A" converter) and L23	12
<input type="checkbox"/>	L28	("D/A" converter) and L22	31
<input type="checkbox"/>	L27	((EL) or (electro-luminescence)) and L16	442
<input type="checkbox"/>	L26	((EL) or (electro-luminescence)) and L15	102
<input type="checkbox"/>	L25	((EL) or (electro-luminescence)) and L14	521
<input type="checkbox"/>	L24	((OLE) or (organic light emitting)) and L16	115
<input type="checkbox"/>	L23	((OLE) or (organic light emitting)) and L15	105
<input type="checkbox"/>	L22	((OLE) or (organic light emitting)) and L14	308
<input type="checkbox"/>	L21	(register\$ same latching) and L13	2
<input type="checkbox"/>	L20	(register\$ same latching) and L12	84
<input type="checkbox"/>	L19	(register\$ same latching) and L11	15
<input type="checkbox"/>	L18	(register\$ same latching) and L10	47
<input type="checkbox"/>	L17	register\$ and L13	36
<input type="checkbox"/>	L16	register\$ and L12	1559
<input type="checkbox"/>	L15	register\$ and L11	264
<input type="checkbox"/>	L14	register\$ and L10	963
<input type="checkbox"/>	L13	(345/692)[CCLS]	89
<input type="checkbox"/>	L12	(345/204 345/690 345/692)[CCLS]	4159
<input type="checkbox"/>	L11	(345/82)[CCLS]	894
<input type="checkbox"/>	L10	(345/55 345/76 345/82)[CCLS]	3131
<input type="checkbox"/>	DB=PGPB,USPT,EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L9	L8 not L6	0
<input type="checkbox"/>	L8	L7 and display	1
<input type="checkbox"/>	L7	L5 and ((latching same data).clm.)	15
<input type="checkbox"/>	L6	L5 and ((latching display data).clm.)	1
<input type="checkbox"/>	L5	L4 and (register.clm.)	890
<input type="checkbox"/>	L4	matsushita-electric-industrial\$.as.	32394
<input type="checkbox"/>	DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ		
<input type="checkbox"/>	L3	L1 and L2	2
<input type="checkbox"/>	L2	date-yoshito\$.in.	31
<input type="checkbox"/>	L1	oomori-tetsuro\$.in.	2

END OF SEARCH HISTORY